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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/320,507 12/17/2002 PAT 6,624,010

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-384956 12/18/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/18/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	JAPAN	DRAWING 3	CLAIMS 14	CLAIMS 1
Verified and Acknowledged  Examiner's Signature	Initials				

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## TITLE

Method of manufacturing semiconductor device having thin film SOI structure

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